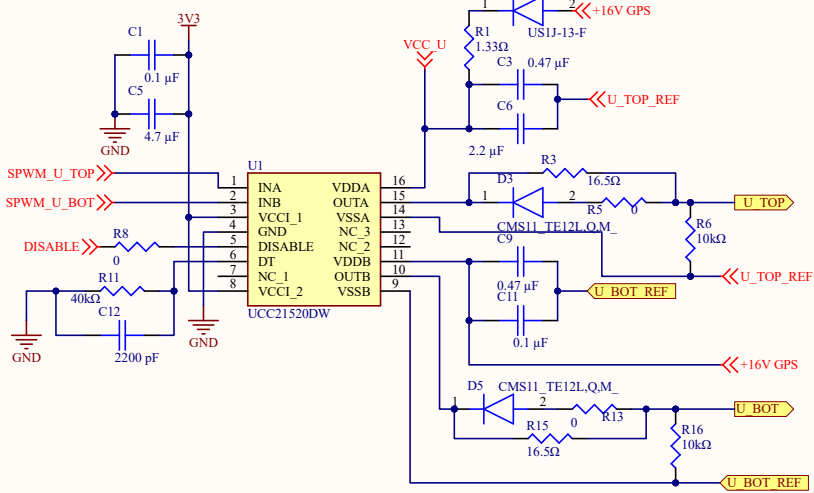
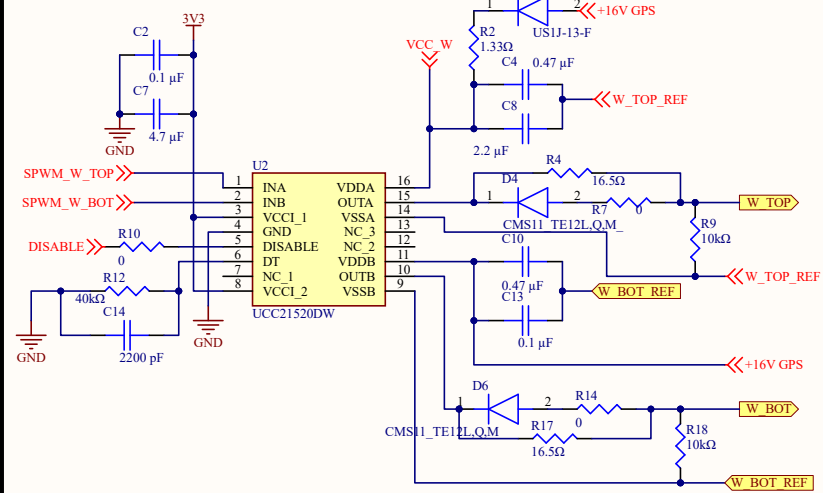


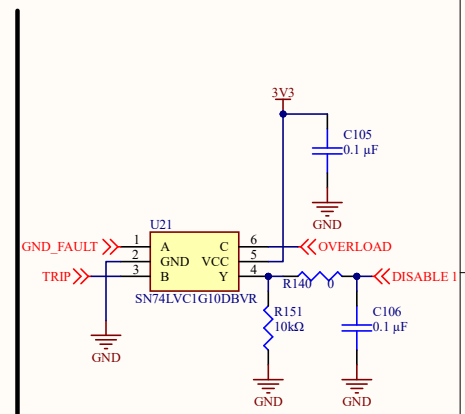
Gate Driver (PHASE U)



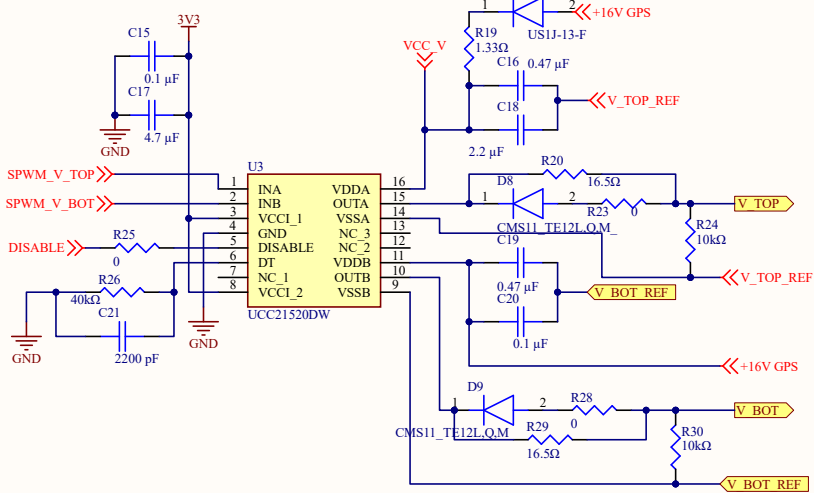
Gate Driver (PHASE W)



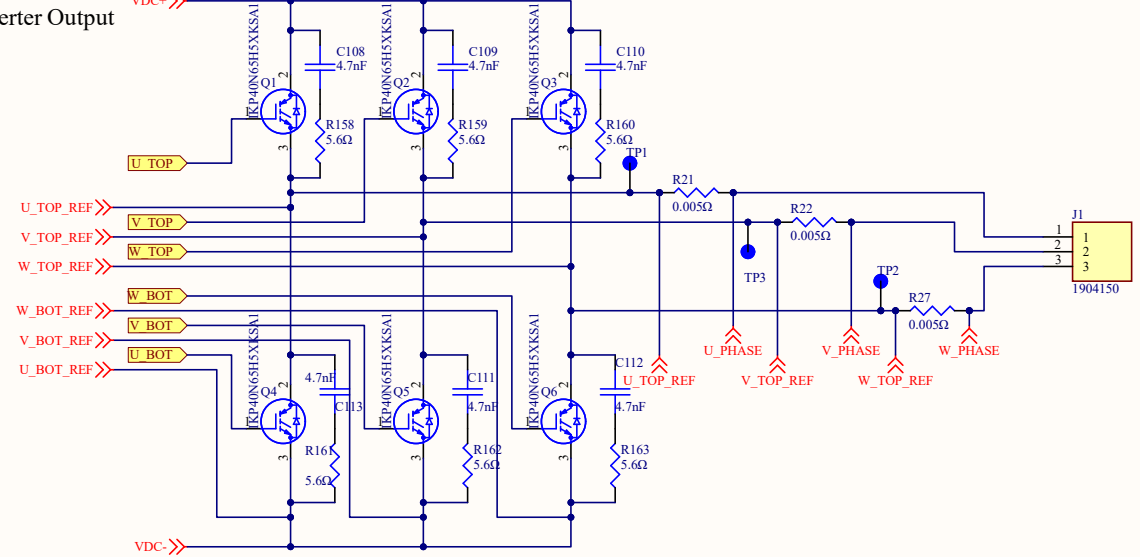
Fault Controller



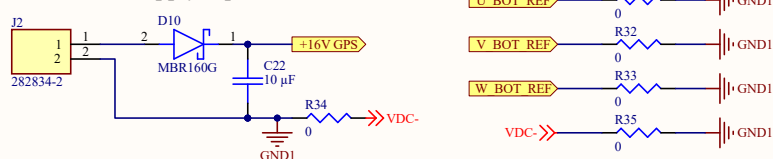
Gate Driver (PHASE V)



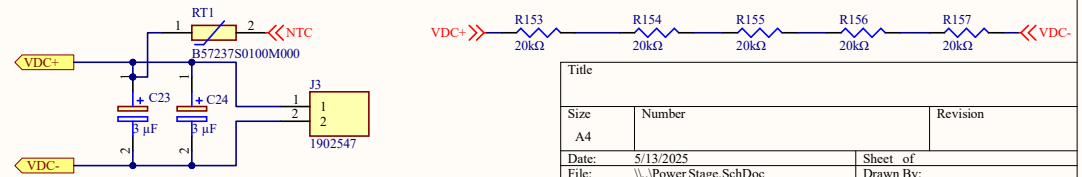
Inverter Output



Gate Driver Supply Input

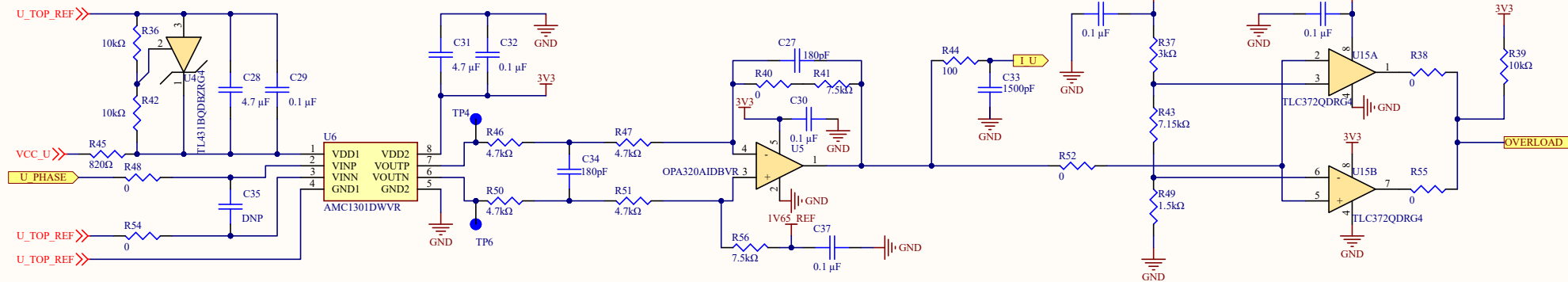


DC Input

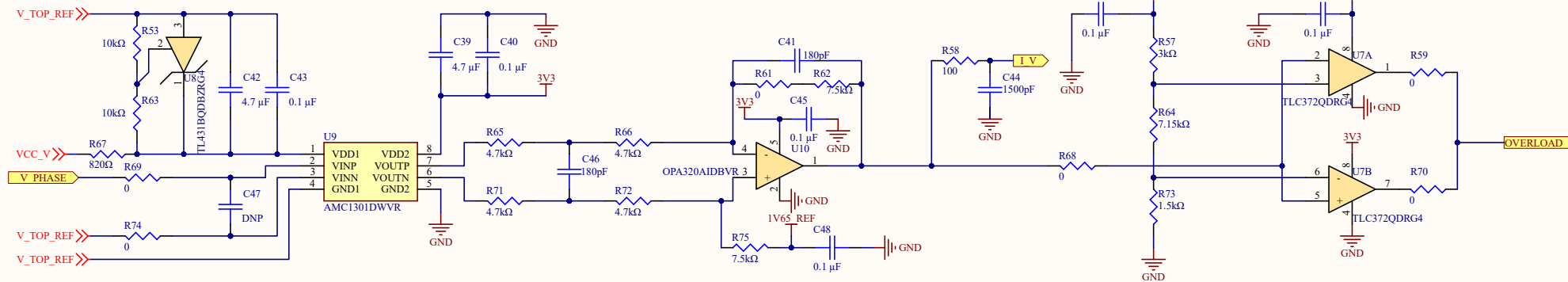


Title		
Size	Number	Revision
A4		
Date:	5/13/2025	Sheet of
File:	\\.\Power Stage.SchDoc	Drawn By:

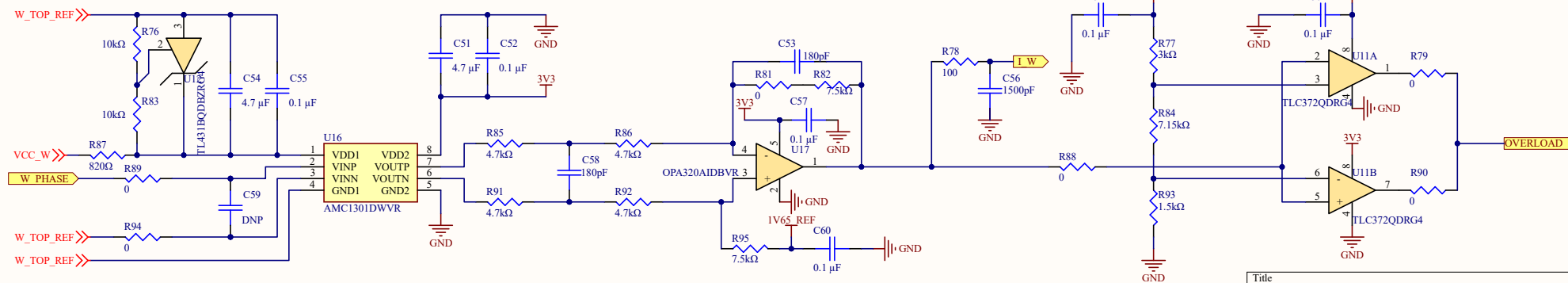
SENSING U PHASE



SENSING V PHASE

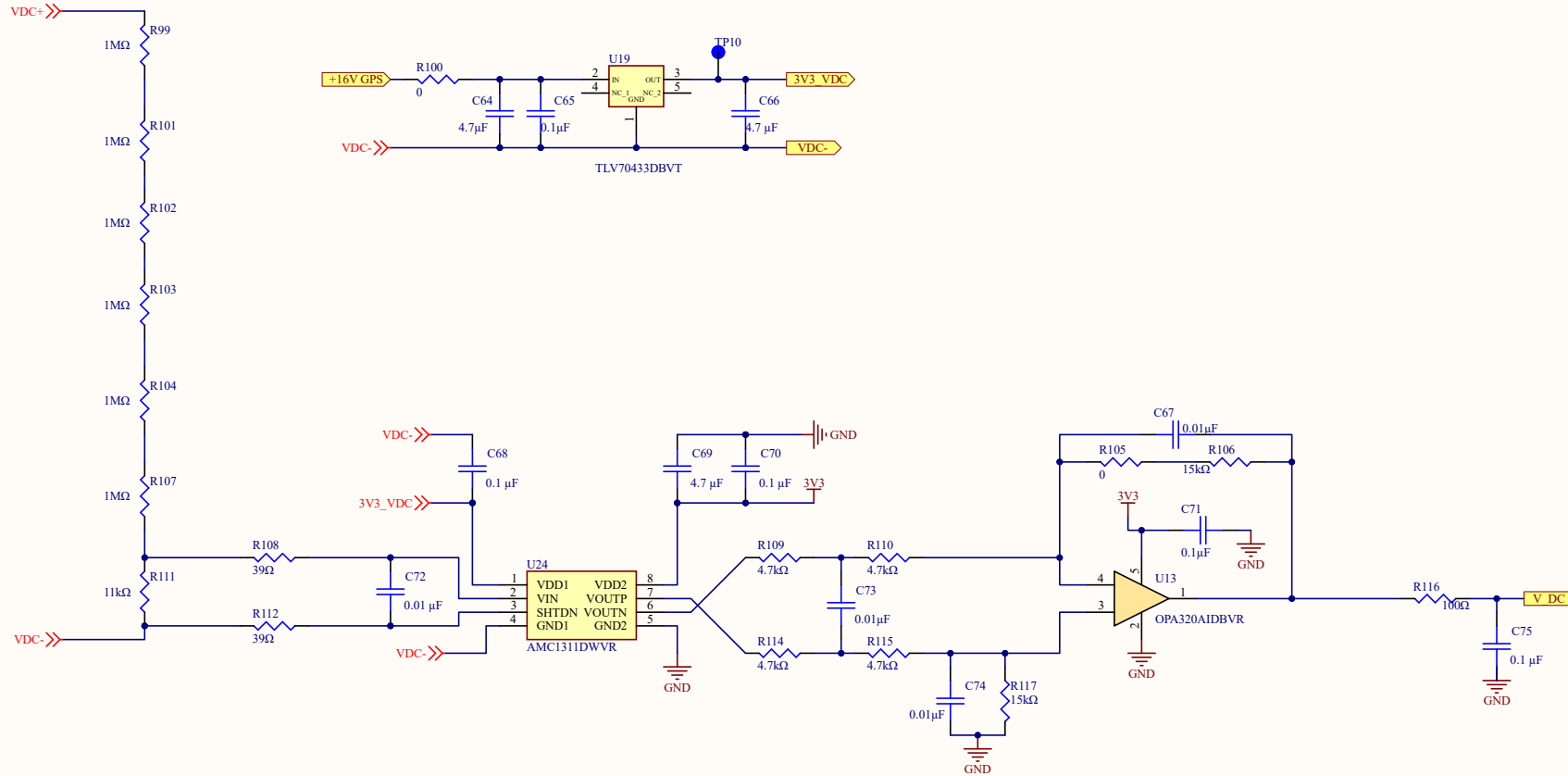


SENSING W PHASE

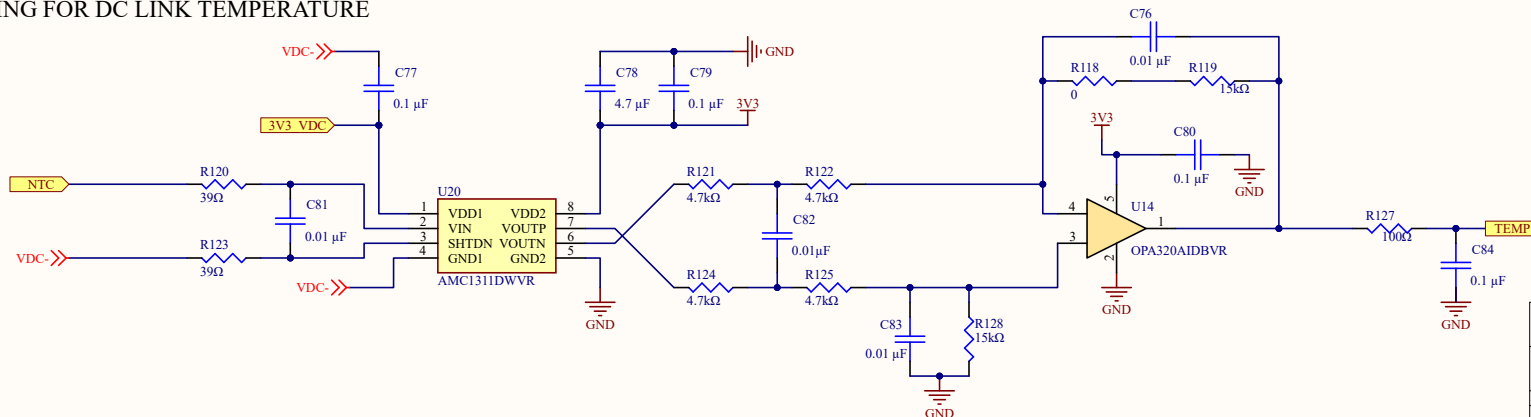


Title		
Size	Number	Revision
Date:	5/13/2025	Sheet of
File:	\\CURRENT SENSING.SchDoc	Drawn By:

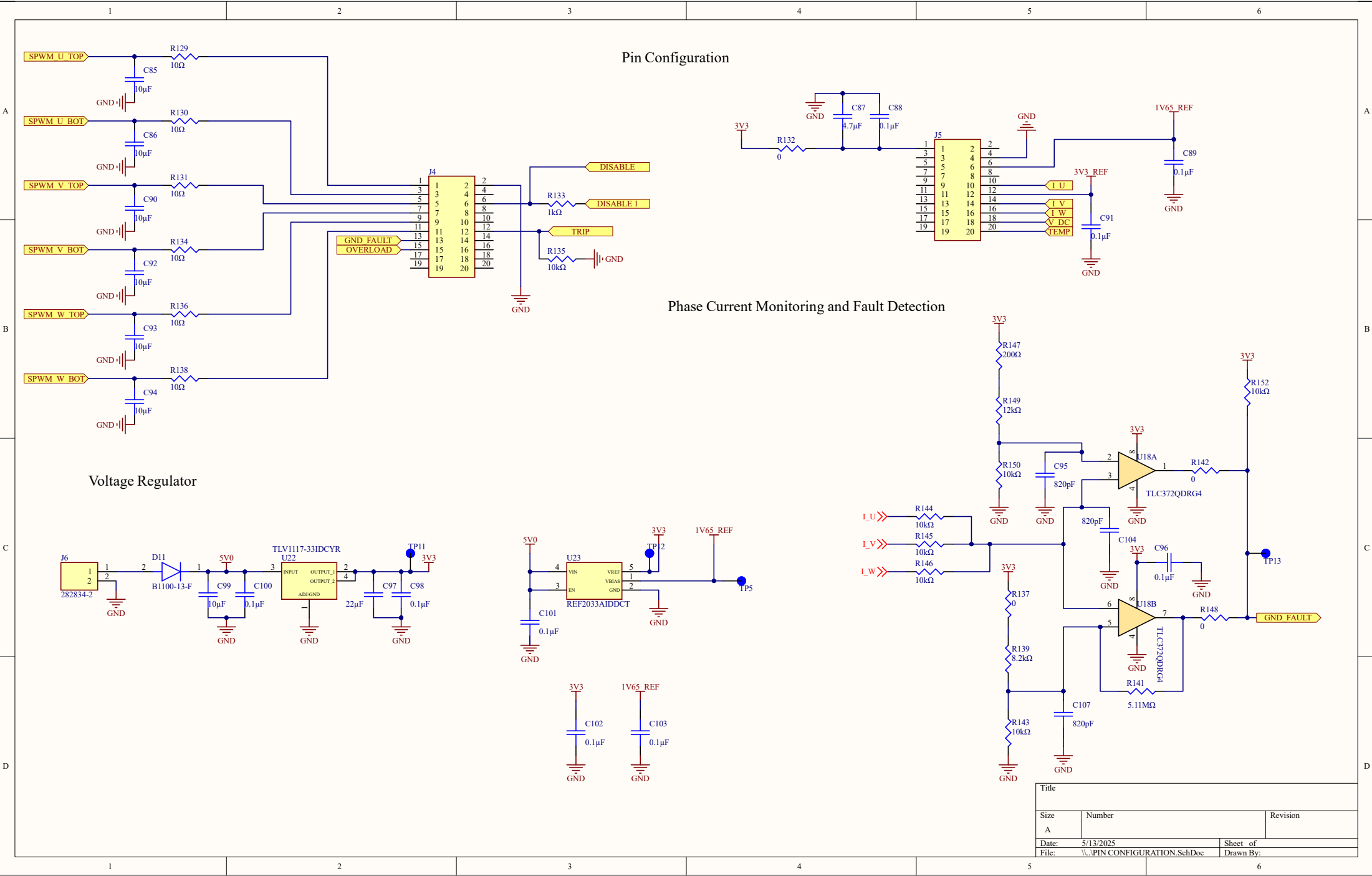
SENSING FOR DC LINK VOLTAGE



SENSING FOR DC LINK TEMPERATURE



Title		
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A		
Date:	5/13/2025	Sheet of
File:	\\.\VOLTAGE_SENSING.SchDoc	Drawn By:



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File:	\\...PIN CONFIGURATION.SchDoc	Drawn By: